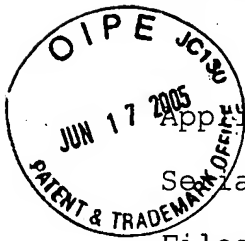


FDJ

U.S.S.N. 10/632,379



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Fu-Liang Yang

Group Art Unit: 2829

Serial No.: 10/632,379

Examiner: Dana Farahani

Filed: 08/01/2003

In Response to Office Action

Dated: 03/03/2005

For: FIELD EFFECT TRANSISTOR (FET) DEVICE HAVING CORRUGATED
STRUCTURE AND METHOD FOR FABRICATION THEREOF

Attorney Docket No.: 67,200-377

Certificate of Mailing

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Va. 22313-1450.

Date: June 15, 2005

Kathy Dixon

Name of person mailing papers

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, Va 22313-1450

Dear Sir:

In response to an Office Action mailed 03/03/2005
please enter the following amendments and consider the following
remarks.

Attached is a Petition for Extension of Time, together
with a Credit Card Payment form, extending the reply time to July
3, 2005.